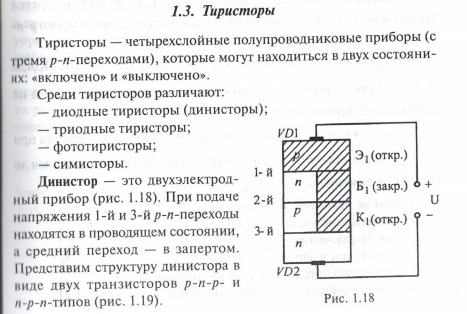
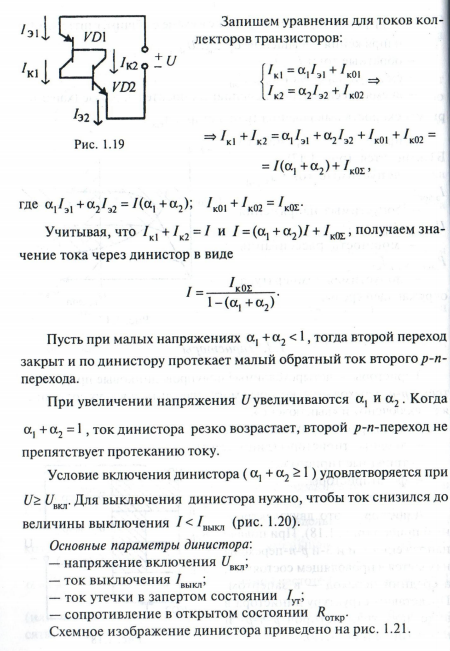
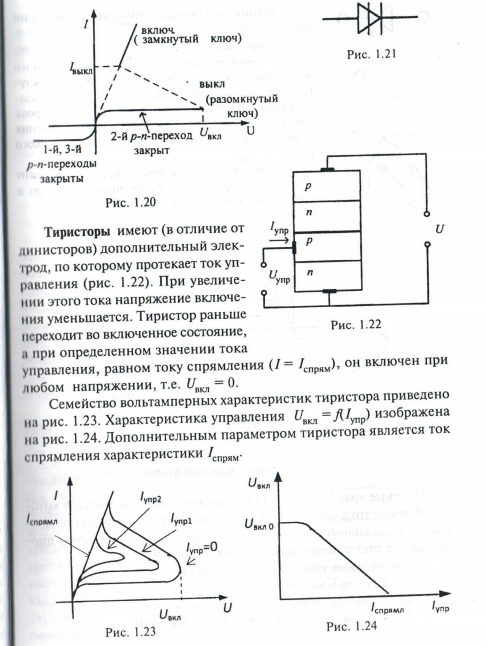
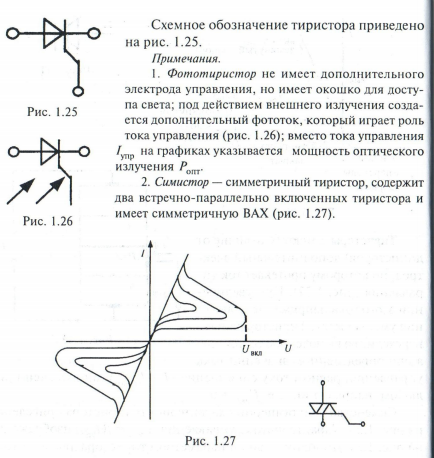
**7. Тиристоры, структура, характеристики, параметры.**

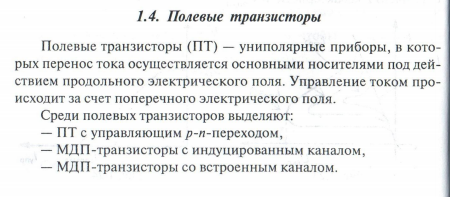
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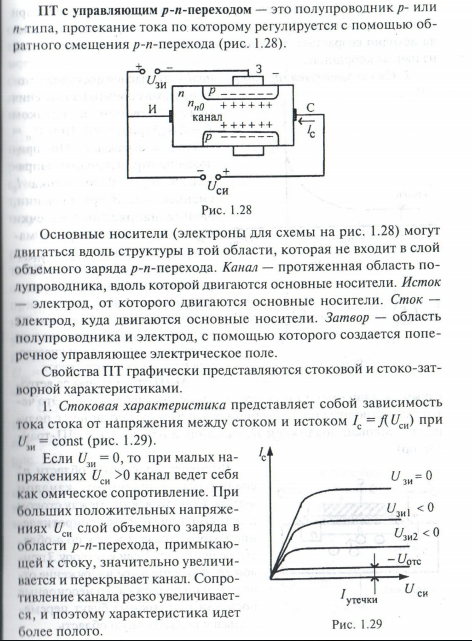
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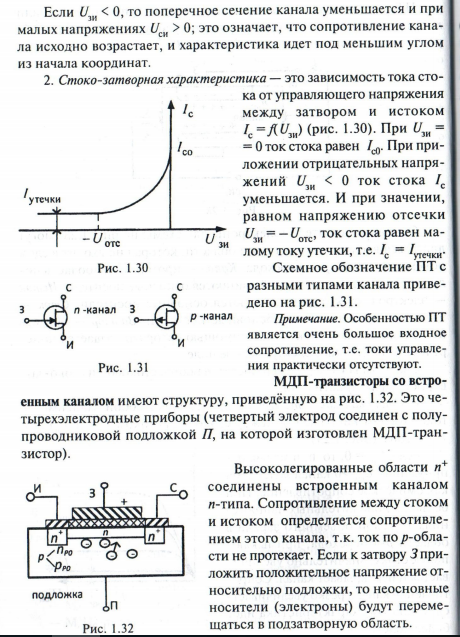
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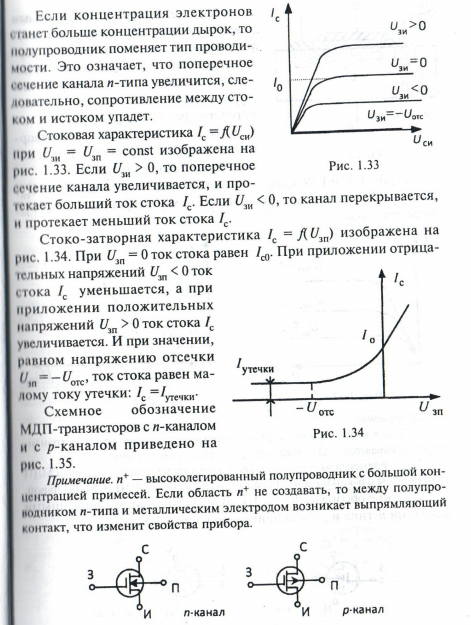
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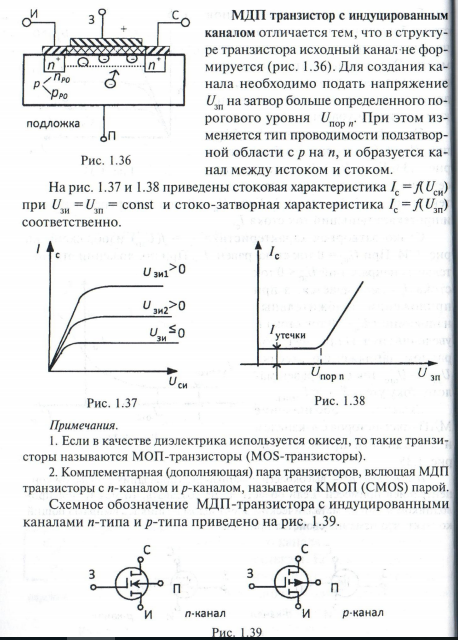
**8. Полевой транзистор с управляющим p-n-переходом.**

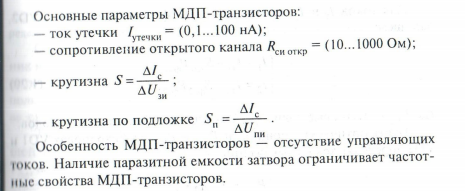
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